

Fig. 3

< m D 3 **1** → . 🖒 5:43PM Profile Tolarance
371.25 Sq. In. / Manufacturing Panel (22.5 X 16.5)
0 Up Manufacturing Panel
0.300 | < Board step (standard is .3")</p> Internet **©** 0 GO TO SAVED 0.001 Annular Ring (Inner Layers) 0.002 Annular Ring (Outer Layers) 226 CLEAR ALL PRINT <<Symmetric Construction of Multi-Layer Recommended 2 Option Option SAVE/COMPARE 1 Option ٦. 듬 Outer Layer Hole Tolerance
 Min. Pad Size (Inner)
 Inner Layer Hole Tolerance 3.1:1 Aspect Ratio < Min. Pod Size (Outer) ,506 < Holes < or = 0.020" Fixtures/ Tooling Retail Price/ Board lacksquare~202 208 -208 204 \$0.00 \$0.00 Assistant Tools Bare Board -/- .010 HASL (Hot Air Solder Leveled) >=.002 >=.003 Length 0.500/0.500 \$\frac{1}{2}\text{Favorites} 🗗 Bare Board Design Assistant – Microsoft Internet Explorer ¥ Min. Finished Plated Through Hole (Outer Layer) Plated Thru Hole Prep.
Min. Finished Plated Through Hole (Inner Layer)
Moximum Finished Plated Through Hole (Inner Layer)
Enter Total * Different Through Hole Sizes
Enter Total * Different Through Hole Sizes Search Panel border X direction (standard is .75") 208 208 Bare Board Design Assistant Finished Board Thickness (inches) / Tolerance Select Ounces of Finished Copper (Outer Layers) Select Ounces of Copper (liner Layers) Select Surface Finish over Copper Set Monitor to 1024 x 768 Pixels
Monitor A 85 Z00M Annular Manufacturing Panel UtilizationX (Calculated)
Number of Boards Manufacturing Panel (Calculated) Bare Board Des... Help \$ Hold Curser over a question for description 🗢 Tools Design Single Board or Multi-Board Array?

(3)

×

①

Bock

Address

File Edit View Favorites

7

Þ

E

Are there any Plated Slots?

Bstart

6

Select Laminate Material Enter Quantily Turnaround Required? Select type of Board



EBare Board Design Assistant - Microsoft Internet Explorer File Edit View Forcetites Tools Help Each - A X Z A D Search X Forcetites A Wedla P A D Unks
Design Assistant Tools Bare Board
Bare Board Set Mapilior Monitor & Bare and or Multi-Board and or Multi-Board and or Multi-Board side Board Dimen none border X direct nones of Board neman Markeriol Board Thickness of finished Plated Throug ru Hole Prep had Plated Throug Finished Holes ony Butled Holes ony Butled Holes ony Butled Holes
Done
#Start) € Bare Board Design As

Fig. 5

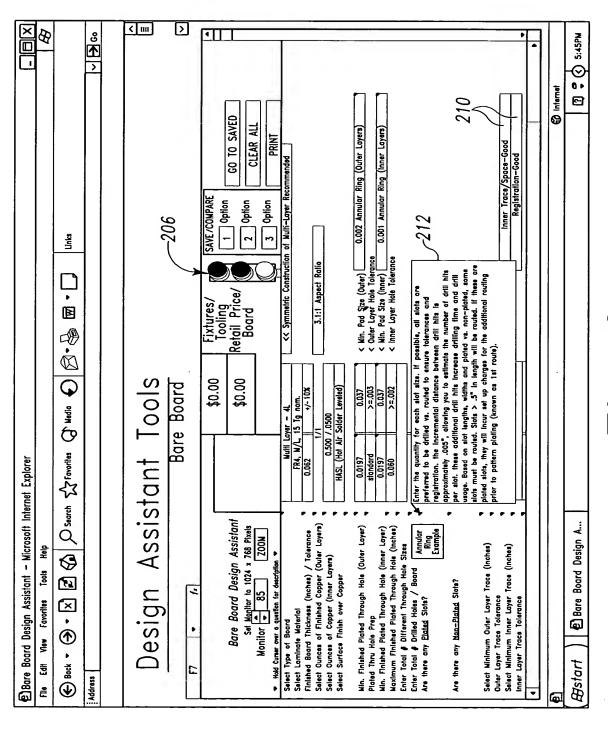


Fig. 6

2,200

Bare Board Design Assistant - Microsoft Internet Explorer		
File Edit View Favorites Tools Help		B
ors ○ ③ ② ② × · ◆ · × pool ◆	O Search A Fowerlies G Media O 18 B T Units	
Address	予	3 1
Design Assi	Assistant Tools	< un
D29 + fs	Bare Board	[>]
	SAVE / COMPARE	TI I
Bare Board Design Assistant Set Maplior to 1024 x 768 Pixels	\$0.00 Retail Price/	
Monitor	3 Option	
Salect Ounces of Finished Copper (Outer Layers) Salect Ounces of Copper (Inner Layers) Salect Surface Finish over Copper	1/1 3.1:1 Aspect Ratio 0.500/0.500 HASL (Hot Air Solder Leveled)	
Min. Finished Plated Through Hole (Outer Layer) Plated Thru Hole Prep Min. Trilshed Plated Through Hole (Inner Layer)	0.0197 0.037 0.04 Size (outer) 0.002 Annular Ring (Outer Layers) couter Layers 0.0197 0.037 0.037 0.0197 0.007 0	
Are there ony Non-Plothd Slots? 214	V 200	
Select Minimum Outer Layer Trace (Inches)	0.007 0.007 < Outer Layer Space	
Select Minimum Inner Layer Trace (Inches) Inner Layer Trace Tolerance Inner Layer Trace Tolerance	0.007 7-302 < Inner Layer Space Inner Trace/Space-Good Registration-Good No No No No No No No	
Solder Mask Needed?	Tes Green < Solder Mask Color	•
		7
a	(6) Infernet	
(#Start) Bare Board Design	NY81:8 Ø ② ♣ ②	9:19AM

Fig. 7



Bare Board Design Assistant - Microsoft Internet Explorer	
File Edit View Fovorites Tools Help	
← Bock • → X Z ← X Search ☆ Fovorites ← Wedio ← 🖂 🖼 • 🔲 Links	
Address	° •
Design Assistant Tools	< 000
Bare Board	Ŋ
\$0.00 Fixtures/ (SAVE/COMPARE	1
d Design Assistant \$0.00 Retail Price/	
Layers) 1/1 3.1:1 Aspect Ratio	
HASI (H	
0.0197 0.037 < Win. Pad Size (Outer)	
Min. Flished Ploted Through Hole (Inner Layer) 0.050 >=.002 < Min. Pod Size (Inner) 0.001 Annular Ring (Inner Layers)	
Enter loting pulled Hoiss / Board Annular Ves (1 Size) Ves (1	
, so the state of	
Select Minimum Guter Loyer Trace (Inches) 0.007 0.007 0.007 course Invest Trace Trace (Inches)	
Trace (inches) 9	
No	
Solder Mosk Needed? • • 188 Urben < Solder Mosk Color	P •
(B) Done	ta
(Bestart) 🖻 Bare Board Design	₩861:8 Ø Ø

Fig. 8



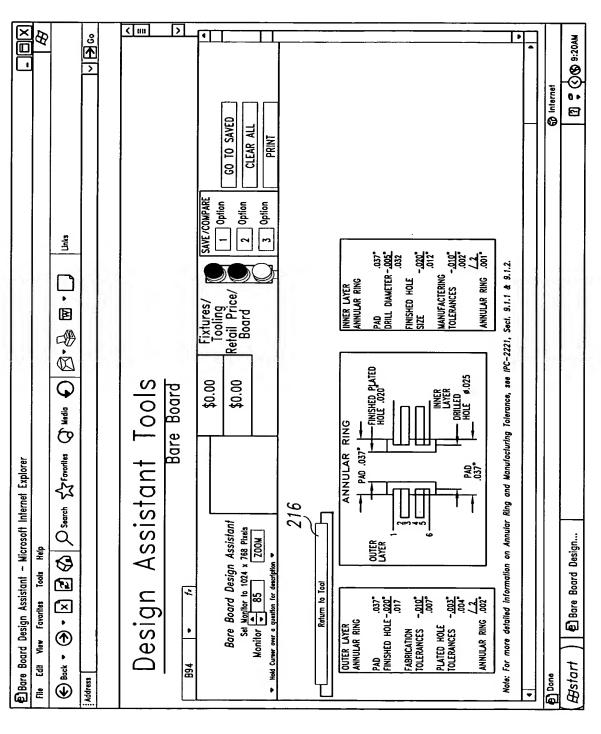


Fig. 9



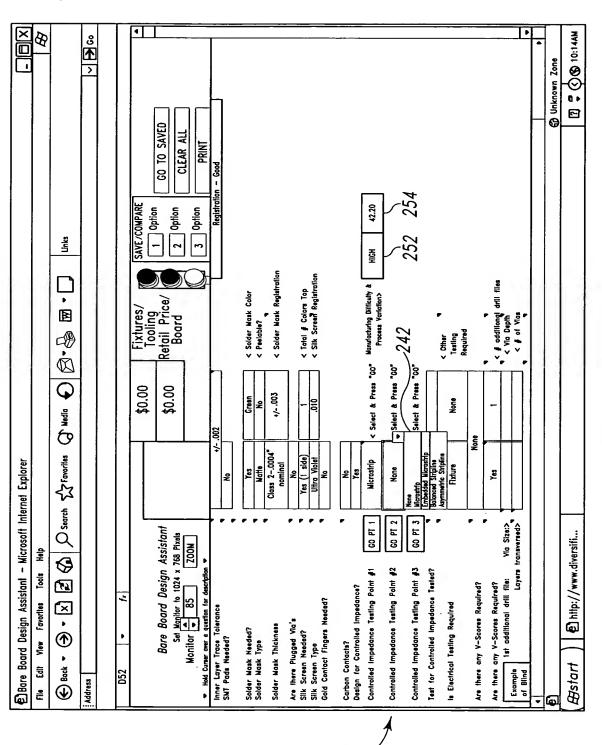


Fig. 10

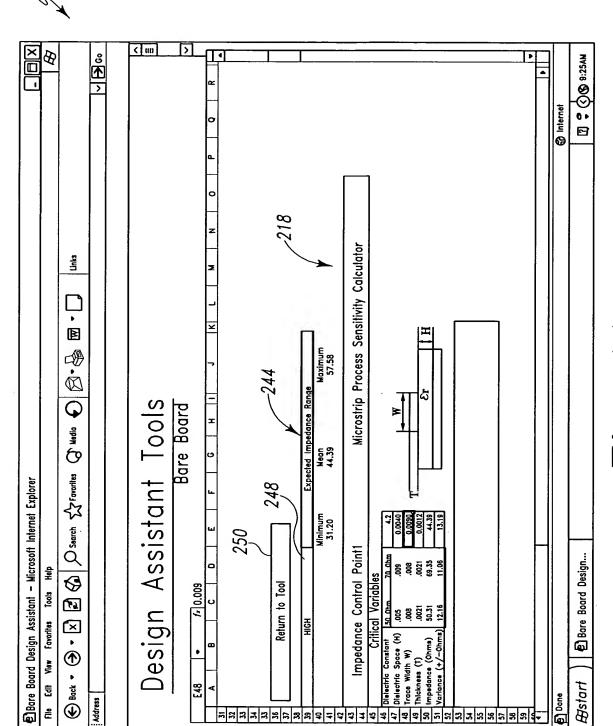


Fig. 11

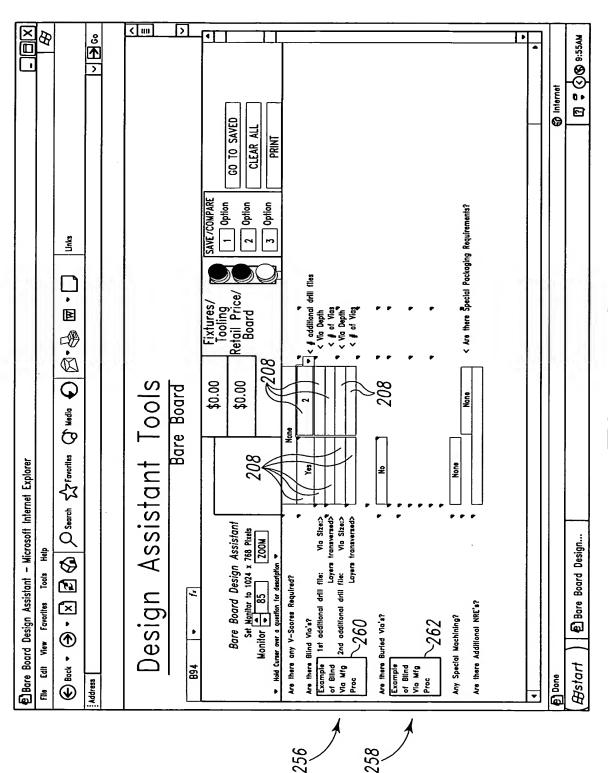


Fig. 12



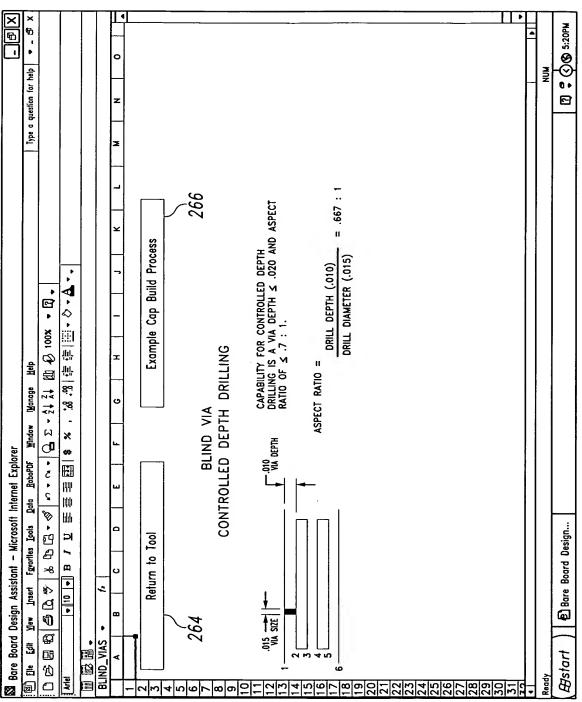


Fig. 13



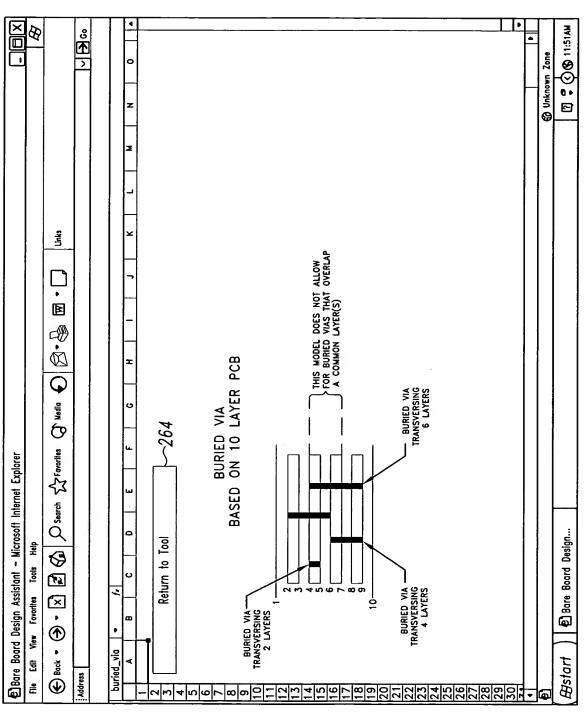


Fig. 14

B **⊗⊗** 10:17AM ვ **1** Unknown Zone > 0 + 🖂 6 232 Option 3 Clear 3 1,986.23 62.22 HASL (Hot Air Solder Leveled) Muli Layer - 4L FR4, M/L, 175 Tg nom. 0.062 | +/-1 Tooling Retail Price/ Board Links Fixtures/ -228 Clear 2 1,812.10 B 40.41 HASL (Hot Air Solder Leveled) 232 Option 2 Retall Price/ Board Tooling Fixtures, 0.007 Media Ö Clear 1 1,912.10 50.53 HASL (Hot Air Solder Leveled) 0.007 🔁 Bare Board Design Assistant – Microsoft Internet Explorer Option 1 Bare Board)Design (Assistant Retail Price)
Board Fixtures / Search Bare Board Design... Board/Array Dimensions

**Couts Permitted / Profile tolerance
Manufacturing Ponel Ultipation / Calcudated)
Number of Board Manufacturing Ponel (calcudated)
Ponel Boards X Direction / Y Direction
Board Site
Ents Quantity
Turneround Required
Type a Board
Laminate Martial
Filished Board Thickness (Inches)/Tolerance
Concess of Copper (Outer Layers)
Surface Finish over Copper
Nin. Finished Plated Thru Hole / Pod (Oute
Min. Finished Plated Thru Hole Sizes
Coverage x over Copper
Nin. Finished Plated Thru Hole Sizes
Finer Total & Different Thru Hole Sizes
Collid & Dullide Holes/Board / ¥ under/O.00
Plated Siats / § of Locations
Langth of Stat # / Width of Stat # I
Platerance of Stat # / Width of Stat # I
Platerance of Stat # / Width of Stat # I
Platerance of Stat # / Width of Stat # I
Platerance of Stat # / Width of Stat # I
Platerance of Stat # / Width of Stat # I
Platerance of Stat # / Width of Stat # I
Platerance of Stat # / Width of Stat # I
Platerance of Stat # / Width of Stat # I
Platerance of Stat # / Width of Stat # I
Platerance of Stat # / Width of Stat # I
Platerance of Stat # / Width of Stat # I
Platerance of Stat # / Width of Stat # I
Platerance of Stat # / Width of Stat # I
Platerance of Stat # / Width of Stat # I
Platerance of Stat # / Width of Stat # I
Platerance of Stat # / Width of Stat # I
Platerance of Stat # / Width of Stat # I
Platerance of Stat # / Width of Stat # I
Platerance of Stat # / Width of Stat # / I
Platerance of Stat # / Width of Stat # / I
Platerance of Stat # / Width of Stat # / I
Platerance of Stat # / Width of Stat # / I
Platerance of Stat # / Width of Stat # / I
Platerance of Stat # / Width of Stat # / I
Platerance of Stat # / Width of Stat # / I
Platerance of Stat # / Width of Stat # / I
Platerance of Stat # / Width of Stat # / I
Platerance of Stat # / Width of Stat # / I
Platerance of Stat # / Width of Stat # / I
Platerance of Stat # / Width Width of Stat # / I
Platerance of Stat # / Width Width Outer Loyer Trace Totacrance 훈 Clear All \$ Single Board or Multi-Board Array / # per

<u>5</u> $leve{x}$ Favorites

Ē Back

Ē

• **①** View

PRINT

RETURN

22

Address

Bstart